

IN THE CLAIMS

Please amend Claims 1 – 11 as follows:

1. (PREVIOUSLY PRESENTED) A data carrier that is arranged to receive a signal in a non-contacting manner and that has an electrical circuit, to which circuit the signal can be fed and which circuit is arranged to generate a supply voltage for parts of the circuit by using the signal, which circuit comprises storage means that are arranged to store information capacitively, the information being represented by a value of an information voltage arising at the storage means, which value of the information voltage is at most equal to the value of the supply voltage, and which circuit has evaluation means to which the information voltage can be fed and that are arranged to evaluate the information voltage, with the help of a comparison voltage, for the information represented by the information voltage, characterized in that comparison-voltage generating means that are arranged to generate and emit the comparison voltage are implemented separately from the evaluation means, and in that the evaluation means are arranged to receive the comparison voltage from the comparison-voltage generating means.
2. (PREVIOUSLY PRESENTED) A data carrier as claimed in claim 1, characterized in that the evaluation means are formed by a difference amplifier stage, which difference amplifier stage is intended to amplify the difference between the information voltage and the comparison voltage.
3. (PREVIOUSLY PRESENTED) A data carrier as claimed in claim 1, characterized in that the circuit has a test terminal from which a representation of the information voltage can be picked off, and in that the evaluation means are arranged to make the information voltage available at the test terminal in a way that can be controlled.
4. (PREVIOUSLY PRESENTED) A data carrier as claimed in claim 1, characterized in that the comparison-voltage generating means are arranged to take account of a value of the supply voltage when generating the comparison voltage.
5. (PREVIOUSLY PRESENTED) A data carrier as claimed in claim 1, characterized in that the comparison-voltage generating means are arranged to generate the comparison voltage in a programmable manner.

6. (PREVIOUSLY PRESENTED) A circuit for a data carrier, which data carrier is arranged to receive a signal in a non-contacting manner, which circuit is arranged, by using the signal, to generate a supply voltage for parts of the circuit, which circuit comprises storage means that are arranged to store information capacitively, the information being represented by a value of an information voltage arising at the storage means, which value of the information voltage is at most equal to the value of the supply voltage, and which circuit has evaluation means to which the information voltage can be fed and that are arranged to evaluate the information voltage, with the help of a comparison voltage, for the information represented by the information voltage, characterized in that comparison-voltage generating means that are arranged to generate and emit the comparison voltage are implemented separately from the evaluation means, and in that the evaluation means are arranged to receive the comparison voltage from the comparison-voltage generating means.

7. (PREVIOUSLY PRESENTED) A circuit as claimed in claim 6, characterized in that the evaluation means are formed by a difference amplifier stage, which difference amplifier stage is intended to amplify the difference between the information voltage and the comparison voltage.

8. (PREVIOUSLY PRESENTED) A circuit as claimed in claim 6, characterized in that the circuit has a test terminal from which a representation of the information voltage can be picked off, and in that the evaluation means are arranged to make the information voltage available at the test terminal in a way that can be controlled.

9. (PREVIOUSLY PRESENTED) A circuit as claimed in claim 6, characterized in that the information-voltage generating means are arranged to take account of a value of the supply voltage when generating the comparison voltage.

10. (PREVIOUSLY PRESENTED) A circuit as claimed in claim 6, characterized in that the comparison-voltage generating means are arranged to generate the comparison voltage in a programmable manner.

11. (PREVIOUSLY PRESENTED) A circuit as claimed in claim 6, characterized in that the circuit is implemented in the form of an integrated circuit.